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				Application Number	Not Yet Assigned	
INF	ORMATION	DIS	CLOSURE	Filing Date	Herewith	
STA	TEMENT B	Y A	PPLICANT	First Named Inventor	MO et al.	
				Art Unit	Not Yet Assigned	
	(use as many sheets as necessary)			Examiner Name	Not Yet Assigned	
Page	1	of	5	Attorney Docket Number	018865-001740US	

		Document Number			
xaminer	Cite No.	Number Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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Page	3	of	5	Attorney Docket Number	018865-001740US

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Page 5 of 5	Attorney Docket Number	018865-001740US			

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	EN	CHANG, T.S. AND CRITCHLOW, D.L., "Verticle FET random-access memories with deep trench isolation," IBM Technical Disclosure Bulletin, January 1980, pp. 3683-3687, Vol. 22(8B).	
	EO	GRANT, D.A. AND GOWER, J., "The development of power MOS devices," Chapter 1.2 In <u>Power Mosfets: Theory and applications</u> . John Wiley & Sons; New York, 1989, pp. 5-23.	\
	EP	GOODENOUGH, F., "Dense MOSFET enables portable power control," <i>Electronic Design</i> April 14, 1997, pp. 45-50.	\
	EQ	HOLMES, F.E., AND SALAMA, C.A.T., "V groove M.O.S. transistor technology," <i>Electronic Letters</i> , September 20, 1973, pp. 457-458, Vol. 9(19).	
	ER	LIDOW, A. et al., "Power Mosfet technology," IEEE Technical Digest-Int. Electron Devices Meet. 1979, pp. 79-83.	1
	ES	LISIAK, K.P. and BERGER, J., "Optimization of nonplanar power MOS transistors," <i>IEEE Transactions on Electron Devices</i> , October 1978, pp. 1229-1234, Vol. Ed-25(10).	\
	ET	OU-YANG, P., "Double ion implanted V-MOS technology," IEEE Journal of Solid-State Circuits, February 1977, pp. 3-10, Vol SC-12(1).	1
	EU	SALAMA, C.A. AND OAKES, J.G., "Nonplanar power field-effect transistors," IEEE Transactions on Electron Devices, October 1978, pp. 1222-1228, Vol. Ed-25(10).	\
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M	EW	SZE, S.M., "P-N- Junction diode," Chapter 2 In Physics of Semiconductor Devices. Second Edition, John Wiley & Sons; New York, 1981, pp. 63-108.	1

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